

**CARBON NANOTUBES BY LOW TEMPERATURE THERMAL CVD PROCESS FOR FED APPLICATIONS.**

*E.ROUVIERE, Th. KREBS<sup>1,2</sup>*

*J.DIJON, Th. GOISLARD, A. FOURNIER, D. ZANGHI<sup>1,3</sup>*

<sup>1</sup> CEA Grenoble, 17, rue des Martyrs, 38054 GRENOBLE cedex 9.

<sup>2</sup> Laboratoire des procédés de traitement de surface, Service des Matériaux et Procédés, Département des Technologies des Energies Nouvelles

Phone : 33(0)4 38 78 91 42 \_ Fax : 33(0)4 38 78 46 21

E-mail : [erouviere@cea.fr](mailto:erouviere@cea.fr)

<sup>3</sup> Laboratoire technologie et composant sur verre, Service technologie et composants optiques, Département d'optronique

Phone : 33(0)4 38 78 43 13 \_ Fax 33(0)4 38 78 51 57

E-mail : [jean.dijon@cea.fr](mailto:jean.dijon@cea.fr)

Multi Walls Nano Tubes (MWNT) distributed uniformly on catalyst metal coated silicon substrate have been synthesized by Thermal Chemical Vapour Deposition using C<sub>2</sub>H<sub>2</sub> at 500 – 650°C. The growth properties are studied as a function of the catalyst layer thickness, deposition temperature, C<sub>2</sub>H<sub>2</sub> flow rate and growth time. Large area of mostly aligned nanotubes exhibiting length up to 3 μm and diameter from 10 to 80 nm have been produced (figure 1). Applications to field emission displays (FED) are demonstrated. Indeed integration of these tubes on triode structure have been done successfully (figure 2). Peak current density up to 5 mA/cm<sup>2</sup> have been measured with 115 V on the gate and 1000V on the anode (figure 3).

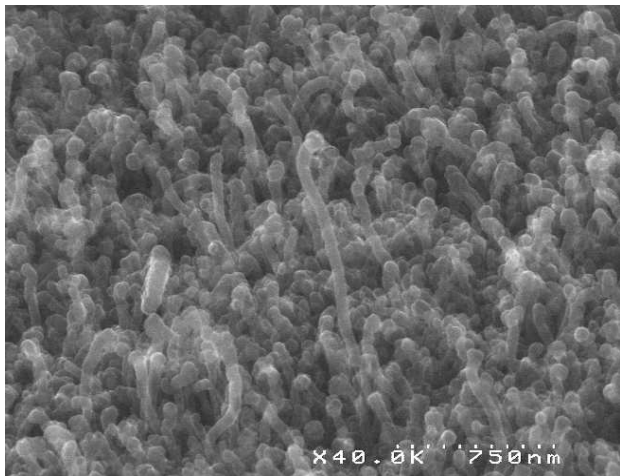


Fig. 1 : SEM micrograph of nanotubes grown by thermal CVD using acetylene gas.

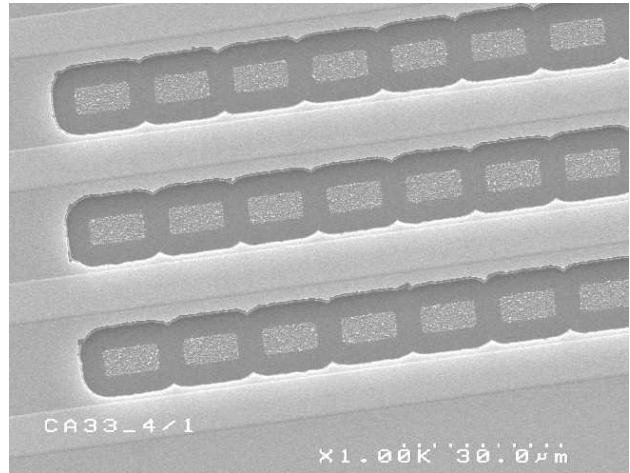


Fig. 2 : SEM micrograph of nanotubes grown on Cathode structures.

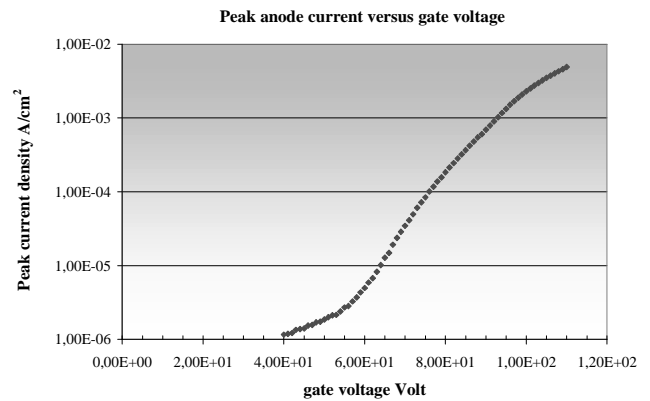


Fig3: Emission characteristic obtained with the triode structure. Vanode=1000V anode cathode gap 1mm.