

**STUDY OF ORGANIC FIELD-EFFECT TRANSISTORS FROM POLY-3-OCTYLTHIOPHENE SOLUTIONS ON DIFFERENT GATE DIELECTRICS**

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One promising field of application of organic transistors is electronics on flexible substrates. In order to achieve this, the thermal SiO<sub>2</sub> dielectrics often used for research purposes has to be replaced by a gate dielectric that can be deposited at low temperatures. On one hand side, this dielectrics should be processable from solution in order to ease the process, to enable printing technologies, and to end up with a fully organic low-cost transistor. On the other side the gate capacitance should be as high as possible in order to allow low gate voltages for current control. This calls for thin dielectrics with high permittivity and high ultimate field strengths. Unfortunately, common soluble polymer dielectrics do not show high permittivity, and common anorganic thin film dielectrics have to be vacuum deposited to keep their good electrical properties.

We developed and studied organic thin film transistors using commercially available poly-3-octylthiophene (P3OT, Aldrich) as semiconductor on different gate dielectrics from different processes on rigid substrates of Si/SiO<sub>2</sub>/Al and Si, respectively: Benzocyclobutene (BCB, Dow) has been spun on from solution, Al<sub>2</sub>O<sub>3</sub> has been anodically grown on aluminum gate from tartaric acid based electrolyte solution, Ta<sub>2</sub>O<sub>5</sub> has been reactively sputtered, and thermally grown SiO<sub>2</sub> for comparison. S/D-electrodes have been evaporated and lift-off patterned for devices of 20 - 50 μm channel length and 1 - 3 mm channel width. P3OT has mainly been spun on from chloroforme solution, additionally we tried ink-jet printing from chlorobenzene solution in order to reduce gate leakage and to take account of patterned gate dielectrics. Fig. 1 display the transistor design schematically.

At first we compared the electrical behaviour of the insulators by investigating metal-insulator-metal- (MIM) structures. Table I gives the extracted data. Because of its high permittivity together with excellent break down characteristics Ta<sub>2</sub>O<sub>5</sub> turned out to be best for gate insulator application. The rather improper performance of BCB is further decreased by the fact, that we did not succeed in keeping the moderate ultimate field strength at films thinner than approx. 500 nm, whereas the inorganic insulators were introduced at 100 nm thickness. The major advantage of anodic Al<sub>2</sub>O<sub>3</sub> is, that it can be grown into holes within photoresist masks, and by this additively patterned already during deposition (fig. 2).

Blanked films of P3OT did not yield strong differences in morphology on the substrates under investigation and we succeed in preparing transistors on all underlayers (fig. 3). Nevertheless, we obtained field-effect mobilities of only 1·10<sup>-4</sup> Vs/cm<sup>2</sup> on BCB, but of up to 2·10<sup>-3</sup> cm<sup>2</sup>/Vs on the inorganics.

Although Ta<sub>2</sub>O<sub>5</sub> is working best among our candidates for gate dielectrics because of highest possible gate charge we suggest anodic Al<sub>2</sub>O<sub>3</sub> for further application because ease of patterning.

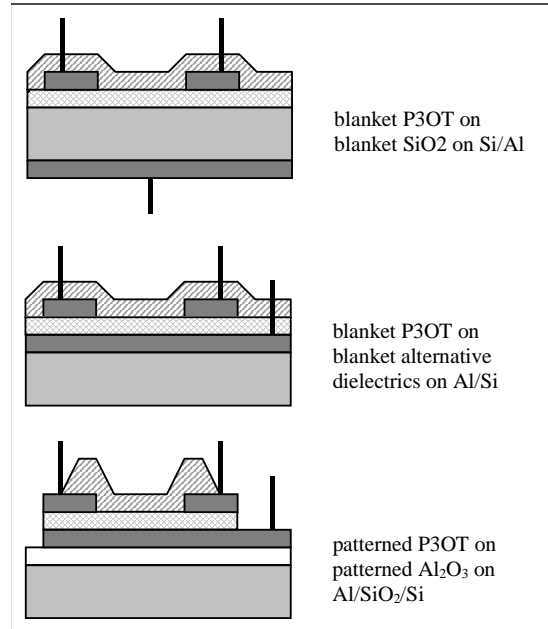


Fig. 1: Transistor design and tips

Dielectric	rel. permittivity $\epsilon_r$	ultim. field strength $E_{bd}$ [MV/cm]	max. gate charge (avg.) (@ $U_T=0$ ) $Q'_{max}$ [10 <sup>-6</sup> As/cm <sup>2</sup> ]
therm. SiO <sub>2</sub>	3.9	5 - 7.5	2.2
BCB	3.0 - 3.4	1 - 3	0.6
anodic Al <sub>2</sub> O <sub>3</sub>	7 - 10.5	2.5 - 5	2.9
Ta <sub>2</sub> O <sub>5</sub>	20 .. 26	4.5 - 5.5	10.2

Table I: Electrical insulator properties

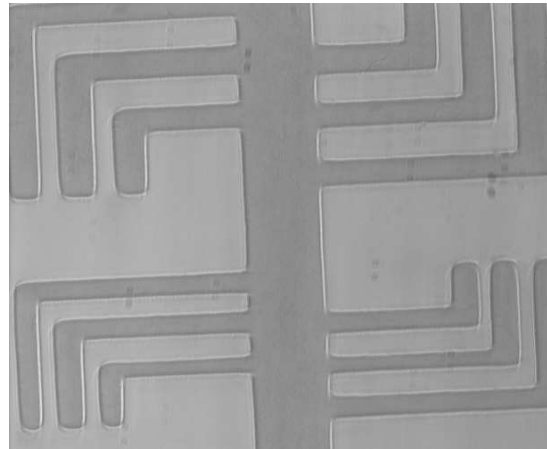


Fig. 2 Additively Patterned anodic Al<sub>2</sub>O<sub>3</sub>

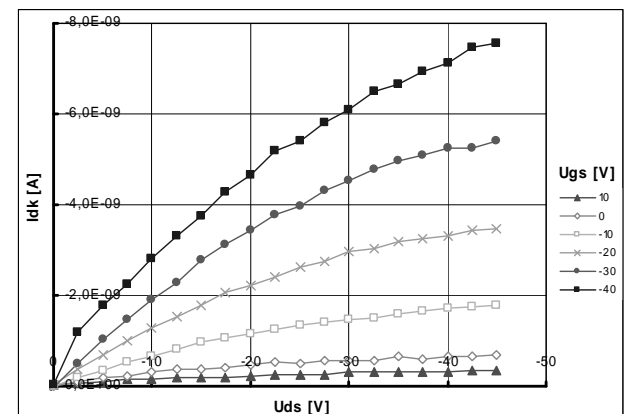


Fig. 3: Output characteristics of ink-jetted P3OT-oFET on BCB (35x1000μm<sup>2</sup> channel)