

Prospects and Challenges for Microphotonic Waveguide Components Based on Si and SiGe

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Research into Si and SiGe as photonic materials has made steady progress since it began two decades ago.¹ Being semiconductors, amplitude and phase modulations can be achieved using carrier injections, as well as thermal effects. Si/Si_{1-x}Ge_x/Si waveguides were the original focus of this field. The refractive index of Si_{1-x}Ge_x increases with the Ge content. The increments are small though, comparable to the typical refractive index contrast in glass-based waveguide systems. There is also a large intrinsic birefringence in SiGe (Fig. 1a), due to the biaxial stress in the pseudomorphic layers.² On the other hand, the material quality of silicon-on-insulator (SOI) has improved significantly over the last 10 years, and SOI has evolved into a platform of choice for integrated photonic circuits. Modulators,³ variable optical attenuators (VOA),⁴ optical switches and passive components such as arrayed waveguide grating (AWG) demultiplexers⁵ have been realized on the SOI platform. Devices with integrated VOAs and AWGs were made commercially available.

To extend the spectral response of photodetectors beyond the Si band-edge, the addition of Ge is a natural choice. With Ge grown on Si, photodetectors with high responsivity beyond 1600 nm and operating at high speeds have been demonstrated.⁶ Embedding SiGe layers in the center of an SOI structure yields well confined waveguides, with the maximum optical power aligned with the SiGe layers to maximize the absorption. Using Stranski-Krastanow growth conditions to form undulating layers, high Ge content can be incorporated without exceeding the kinetic stability limit (Fig. 2a). In waveguide photodetectors using these structures, responsivities of 0.1 A/W were demonstrated for $\lambda=1520$ nm. With silicides serving both as the electrodes and lateral claddings (Fig. 2b), high speed operation was achieved. Using selective area deposition, photodetectors and transparent waveguides can be integrated on the same chip. With Si capping layer, metastable SiGe layers maintain their strain and optical properties even under rather severe thermal treatments, making these structures compatible with CMOS fabrication technologies.

One distinct advantage offered by SOI is the capability for device size reduction, owing to the high index contrast. Using sub-micron waveguide cores, components with micron-scale sizes have been demonstrated. However, as the waveguide dimensions shrink, a number of challenges arise, such as interface scattering loss, waveguide to fiber coupling, and polarization sensitivity. Fig. 1b shows that the geometrical modal birefringence in SOI waveguides increases drastically with reduced core dimensions. We have demonstrated that cladding layers can be used to induce stress anisotropy in SOI waveguides (as shown in Fig. 3 (a) and (b)), significantly modifying the modal birefringence via the photoelastic effect.⁷ By adjusting the cladding thickness or the stress level, waveguides with large geometrical birefringence can be rendered birefringence-free (Fig. 3c). It was also reported that the tensile strain residing in Ge as a result of thermal mismatch reduced the bandgap of Ge. The Ge bandgap was further decreased through tensile strain enhancement from forming TiSi₂ at the back of the wafer.⁸ We will discuss various design possibilities opened by stress engineering in optical components based on Si, SiGe or Ge.

Our recent design of a 100 channel SOI microspectrometer with spectral resolution of ~ 0.1 nm and the free spectral range (FSR) of 20 nm occupies less than 5×5 mm² of wafer estate. Microspectrometers with FSR of $\sim 1 \mu\text{m}$ and several thousand channels can be fitted within 2×2 cm². Compared with the current state of the art devices based on free-space diffraction gratings, waveguide microspectrometers present a significant improvement in performance, particularly in resolution and spectral range, along with unprecedented compactness. Given past achievements and new advances,⁹ silicon-based optical platforms promise to fulfil the requirements for a variety of applications in telecommunications, optical interconnects, environmental and bio-sensing.

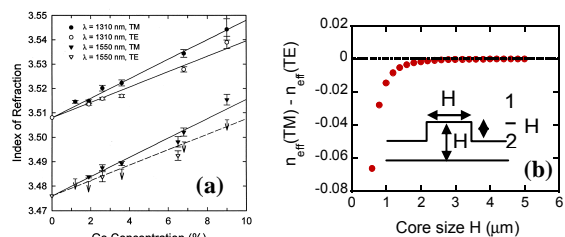


Fig. 1 (a) Variation of Si_{1-x}Ge_x refractive index with Ge content x, for TE and TM polarized light; (b) Modal birefringence due to the waveguide geometry in SOI guides.

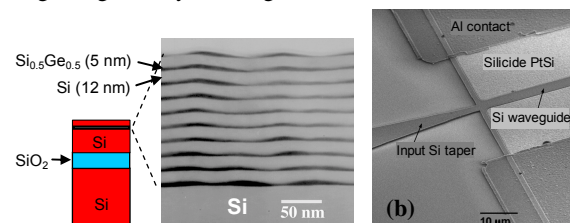


Fig. 2 (a) Undulating SiGe layers embedded in an SOI structure; (b) Waveguide detectors with silicide lateral claddings.

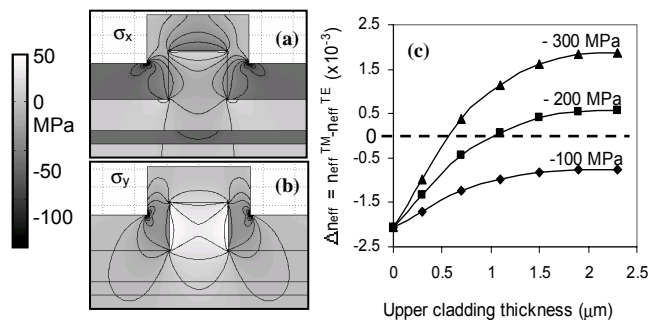


Fig. 3. Stress distributions in a SOI ridge waveguide (a) in-plane and (b) out-of-plane, for cladding stress of -220 MPa (compressive). (c) Modal birefringence as a function of upper cladding thickness, for different cladding stress levels.

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