

### Fabrication of thin relaxed SiGe films for strained Si applications

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Recently there has been a great deal of interest in tensile strained-Si on relaxed SiGe on bulk Si substrates as well as in strained-Si on insulating substrates (SSOI). Most of this work has involved relaxed SiGe buffer films several microns in thickness (1, 2). There have also been efforts to achieve the same results using much thinner relaxed SiGe buffers, on the order of 500nm or less. These methods include implantation of hydrogen or He ions (3) and injection of Si interstitials via low temperature growth (4, 5). Here we present some details on our work using hydrogen implantation.

Compressively strained metastable SiGe films 200-450nm thick were grown at ~500°C using SiH<sub>4</sub> and GeH<sub>4</sub>. The Ge concentration was graded from 20% at the SiGe/Si interface up to 30% or 40% at the top surface. Some constant Ge concentration films were studied, as well. The films were then implanted with hydrogen ions at an energy so that the projected range (R<sub>p</sub>) would be just below the SiGe/Si interface. Subsequent annealing resulted in efficient relaxation of the SiGe films. Figure 1 plots x-ray diffraction (XRD) data for several 21% Ge, 200nm SiGe films. Three films received 3e16 H<sup>+</sup> ions/cm<sup>2</sup> at 25 keV and were annealed at 700 to 900°C. All three became about 80% relaxed, although the 900°C film had a rougher surface. In contrast, a film which was annealed without H<sup>+</sup> implantation barely relaxed at all. Subsequently, it was found that most of the film relaxation occurs within the first minute of annealing (Fig. 2).

Further work has optimized the hydrogen implantation and annealing conditions to achieve high relaxation of SiGe films while maintaining a smooth surface and low defect levels. These films were then used to fabricate strained-Si NMOS and PMOS FET devices. We have also successfully used this process to fabricate SSOI substrates.

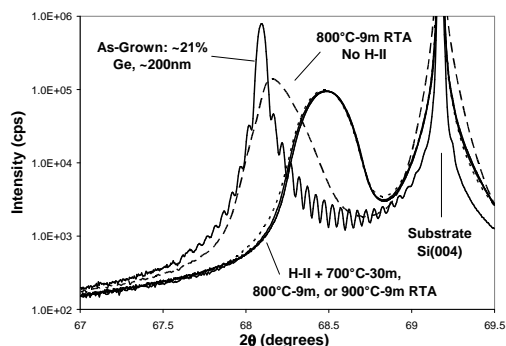


Figure 1. XRD of SiGe films showing effect of hydrogen implantation.

### Relaxation of H<sup>+</sup> implanted SiGe, 170nm, 30% Ge

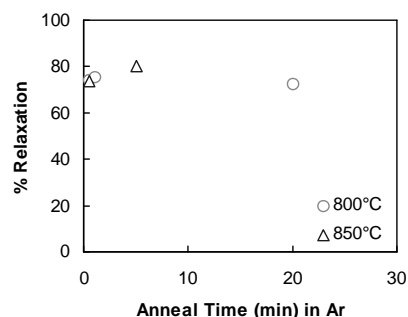


Figure 2. Dependence of relaxation on anneal time.

### References

1. K. Rim, et al., Symp. VLSI Tech., **98** (2002).
2. M.T. Currie, et al., J. Vac. Sci. Technol. B, **19**, 2268, (2001).
3. H. Trinkaus, et al., Appl. Phys. Lett., **76**, 3552, (2000).
4. Kaspar, et al. Thin Solid Films, **336**, 319 (1998).
5. Linder, et al., Appl. Phys. Lett., **70**, 3224, (1997).