

TABLE OF CONTENTS

PREFACE

iii

Poly-Si TFTs from Laser Crystallization Methods

CW Laser Lateral Crystallization (CLC)* N. Sasaki	1
Novel Phase-Modulator for ELA-Based Lateral Growth of Si Y. Taniguchi, M. Jyumonji, H. Ogawa, M. Hiramatsu, and M. Matsumura	18
An Advanced Sample Structure for Excimer Laser Crystallization M. Hiramatsu, H. Ogawa, M. Jyumonji, T. Katou, N. Akita, and M. Matsumura	30
The Electrical Characteristics of the Poly-Si TFTs fabricated by 2-Dimensional Grain Growing ELA method I.-H. Song, H.-S. Shin, and M.-K. Han	38
A New Dual L-Gate Structure of Poly-Si TFT for Suppressing the Kink Effect in SLS/CW Laser Method S.-H. Jung, H.-S. Shin, and M.-K. Han	44

Poly-Si TFTs from Non Laser Crystallization Methods

Reliability Study of MILC Poly-Si TFTs on Plastic Substrates Using PostFlex Transfer Process H. Li and S. Fonash	49
Reduction of the Leakage Current of Thin-Film Transistor on Metal-Induced Laterally Crystallized Polycrystalline Silicon Z. Meng, C. Wu, S. Xiong, X. Shi, H.S. Kwok, and M. Wong	57

TFTs Gate Dielectrics, Doping, Structures, etc.

Low-Temperature Microwave Plasma Oxidation for Gate Dielectrics of Poly-Si TFTs using High-Density Surface Wave Plasma* K. Azuma	63
Fabrication and characterization of n- and p-channel poly-Si TFTs by sputtering deposition of ultra thin gate SiO ₂ films T. Serikawa, M. Miyashita, Y. Uraoka, and T. Fuyuki	74
Ion Shower Doping of Polysilicon Films on Plastic Substrates for Flexible TFT Arrays J. Kim, W.-S. Hong, S. Lee, D.-Y. Kim, J. S. Jung, J.-Y. Kwon, and T. Noguchi	80

* Invited Paper

A Simple CMOS Self-Aligned Double-Gate Poly-Si TFT Technology Z. Xiong, H. Liu, C. Zhu, and J. K. O. Sin	87
Influence of Channel Position on Characteristics of p-Channel c-Si TFT Inside a Location-Controlled Grain V. Rana, R. Ishihara, I. Andel, Y. Hiroshima, D. Abe, S. Inoue, T. Shimoda, J.W. Metselaar, and K. Beenakker	92
High Performance P-Channel Schottky Barrier Thin-Film Transistors with PtSi Source/Drain M.-H. Lee, T.-Y. Huang, K.-L. Yeh, and H.-C. Lin	99
Devices and Modeling	
Hot-Carrier Instability in N- and P-Channel Poly-Si Tofts* H. Tango, M. Suganuma, G. Usami and Y. Nogami	104
Switch-on Undershoot Current Observed in Thin Film Transistors F. Yan, P. Migliorato, and R. Ishihara	112
Modelling of Source Gated Transistors in Amorphous Silicon F. Balon and J. M. Shannon	119
An Investigation of the Electrically Active Defects in poly-Si Thin Film Transistors M. Exarchos, G. J. Papaioannou, D. N. Kouvatsos, and A. T. Voutsas	125
Simulation of Twin Boundary Effect on TFT Characteristics Y. Mo, F. Yan, P. Migliorato, and R. Ishihara	130
Circuits and Systems	
Thin Film Transistor Models for Circuit Simulation* B. Iñiguez, M. S. Shur, T. A. Fjeldly, and T. Ytterdal	136
TFT Mobility Requirement for AMOLED HDTVs* O.-K. Kwon	146
System Displays and TFT Device and Process Technologies* H. Abe	159
High Performance TFT Circuits for all-Integrated Systems on Stainless Steel Foils M. Troccoli, T. Afentakis, T. H. Chuang, Y. L. Chang, M. Hatalis, A. P. Voutsas, J. W. Hartzell, and V. Chouvardas	166

* Invited Paper

Materials and Substrates

Oxide Semiconductors for Flexible Electronics*	178
P. F. Garcia, R. S. McLean, I. Malajovich, and M. H. Reilly	
Thin-Film Transistors Based on Spin-Coated Chalcogenide Semiconductor Channels*	189
D. B. Mitzi, M. Copel, C. E. Murray, L. Kosbar, and A. Afzali	
Hydrogen-Induced Defects in Poly-Silicon Thin Films Observed by Raman Spectroscopy	200
K. Kitahara, Y. Ohashi, T. Yamada, and A. Moritani	
Analysis of Transfer Mechanism in Surface Free Technology by Laser Annealing / Ablation (SUFTLA)*	206
S. Inoue, M. Kasuga, S. Utsunomiya, and T. Shimoda	
High Mobility Top-Gate Micro-Crystalline Silicon TFTs Processed at Low Temperature (<200°C)	215
A. Saboundji, N. Coulon, C. Simon, T. Mohammed-Brahim, O. Bonnaud	
Polysilicon TFTs on Plastic	221
J. Y. Chen	
VHF PECVD Micro-crystalline Silicon Bottom Gate TFT With Thin Incubation Layer	232
J. Li, C. Wu, X. Zhang, Z. Meng, J. Liu, S. Zhao, Y. Zhao, S. Xiong, L. Zhang, and J. Jang	

Organic TFTs

Semiconducting Properties of Pentacene Thin Films Studied by Complex Impedance, Surface Mobility and Photo-induced Effects*	237
A. Toriumi, T. Yokoyama, T. Nishimura, T. Yamada, K. Kita, and K. Kyuno	
Bottom Contact Organic Thin-Film Transistors with Thiol-based SAM Treatment	249
M. Kawasaki, S. Imazeki, M. Ando, and M. Ohe	
Flexible QVGA Active Matrix Displays based on Organic Electronics*	257
F. J. Touwslager, H.E.A. Huitema, G. H. Gelinck, E. van Veenendaal, and P.J. G. van Lieshout	
Study of Organic Field-Effect Transistors from Poly-3-octylthiophene Solutions on Different Gate Dielectrics	263
M. Ploetner, S. Richter, P.-T. Nguyen, H. Heuer, A. Heinzig, T. Wegener, W. Plieth, and W.-J. Fischer	

* Invited Paper

New Applications and Film Preparation Methods

A Novel TFT-Driven Microchannel Electrophoresis Device for Proteins Separation and Identification Y. Kuo and H. H. Lee	270
Hydrogenated Amorphous Silicon Bipolar Junction Thin Film Transistors (a-Si:H B-TFTs) Y. Lei, Y. Kuo, and H. Nominanda	277
Highly pH Sensitive Suspended-Gate Silicon Thin Film Transistor (ISTFT) F. Bendriaa, F. Le Bihan, A.-C. Salaün, T. Mohammed-Brahim, and O. Bonnaud	284
High Dynamic Range Pixel Amplifier Architecture in Amorphous Silicon Technology for Diagnostic X-ray Imaging Applications G. Sanaie-Fard, R Sanaie-Fard, and K.S. Karim	289
New Pixel Circuit to Recover Threshold Voltage Shift in Amorphous Silicon TFT for Active Matrix OLEDs B.-H. You, H.-J. Lee, W.-J. Nam, H.-J. Lee and M.-K. Han	301
Low-Temperature Deposition of Poly-Si and SiGe Thin Films at 450°C and Fabrication of High Mobility TFTs Over 50 CM ² /VS* J.-i. Hanna, J. J. Zhang, J.-W. Lee, and K. Shimizu	308
AUTHOR INDEX	319
SUBJECT INDEX	323