

# Ultrathin Aluminum Oxide Gate Dielectric Prepared by Anodization Followed by Rapid Thermal Anneal

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In this work, we present a novel method to oxidize the aluminum film. Room temperature anodic oxidation in deionized water (DI) water was used to oxidize aluminum for the first time.

Three-inch p-type Si wafers were used. After standard RCA cleaning with final HF dip, 1% Si-Al with different thicknesses of 1.9 nm ~ 4.3 nm were evaporated on the Si wafer.

In the following anodic oxidation procedure, wafer with Al film acts as an anode. Constant electrolyzing voltage of 30V was applied from 5 ~ 30 minutes separately at room temperature using DI water as electrolyte. After anodization procedure, post-anodic-oxidation rapid thermal annealing (RTA) at 900°C for 20sec in N<sub>2</sub> ambient was treated to evolve the hydrogen from the films. After the dielectrics were prepared, standard lithography and etching were utilized to fabricate a MOS capacitor structure.

Figure 1(a) and 1(b) show the Al 2p XPS spectra of original Al and anodic-oxidized Al<sub>2</sub>O<sub>3</sub>, respectively. It is observed that native oxide was existed on the Al surface, but after 10-min anodic oxidation, Al was totally converted into Al<sub>2</sub>O<sub>3</sub>. In addition, we can observe that the binding energy level of anodized Al<sub>2</sub>O<sub>3</sub> is higher than origin one. This is probably due to the interfacial layer grown during anodization is not pure SiO<sub>2</sub> but Al-silicate layer with dielectric constant of 5.5 ~ 6.0 that is higher than conventional SiO<sub>2</sub>. The dielectric constant is estimated to be about ~9.7.

Figure 2(a) and 2(b) shows the effect of N<sub>2</sub> RTA to the anodic-oxidized Al<sub>2</sub>O<sub>3</sub>. It is observed that N<sub>2</sub> RTA can evolve the hydrogen from the anodic-oxidized Al<sub>2</sub>O<sub>3</sub> films; therefore, electron trap density can be significantly reduced.

Figure 3 shows the J-V plot of anodic-oxidized Al<sub>2</sub>O<sub>3</sub> (EOT = 2.5 nm) compared with SiO<sub>2</sub> while device was biased at accumulation region. It is clearly observed that the anodic-oxidized Al<sub>2</sub>O<sub>3</sub> has two order of magnitude lower than SiO<sub>2</sub> for the same EOT.

Figure 4 shows the reliability characteristics of the anodic-oxidized Al<sub>2</sub>O<sub>3</sub> film. The conduction characteristics have very weak temperature dependency and the leakage remains low at T=100°C, indicating a low bulk trap density in the bulk of the film.

Ultrathin Al<sub>2</sub>O<sub>3</sub> film prepared by anodization in DI water was demonstrated to have gate quality characteristics, including low leakage current, low interface and bulk trap densities, and high thermal stability in this work. Beyond thermal oxide method, room temperature anodization may be a promising alternative method to prepare Al<sub>2</sub>O<sub>3</sub> for gate dielectric.

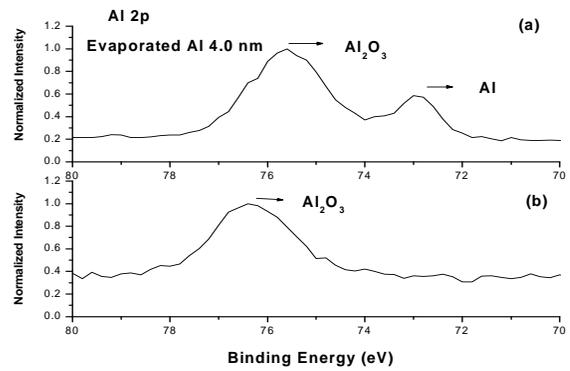


Fig. 1. XPS Al 2p spectra for Al film of (a) original Al film and (b) after 10min anodization.

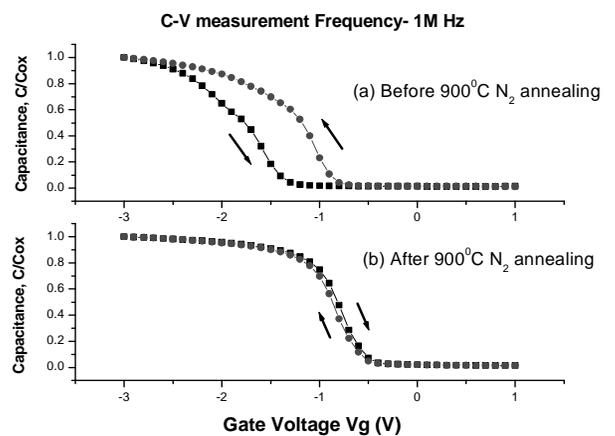


Fig. 2 C-V hysteresis for anodic Al<sub>2</sub>O<sub>3</sub> (a) before N<sub>2</sub> RTA and (b) after N<sub>2</sub> RTA.

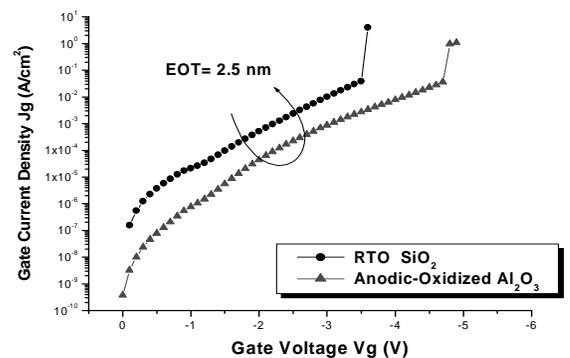


Fig. 3. J-V plot of anodic-oxidized Al<sub>2</sub>O<sub>3</sub> and SiO<sub>2</sub> (EOT=2.5nm)

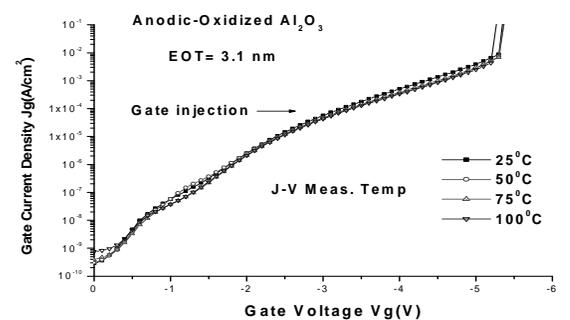


Fig. 4. J-V measurements of anodic-oxidized Al<sub>2</sub>O<sub>3</sub> for temperatures varied from 25°C to 100°C.