Electrical Properties and Physical Model of Porous Silicon Layers

P.Jaguiro^a, S.Lazarouk^b ^a JV "Belaya Vezha", ^b BSUIR, ^a Str. Pionerskaya 7A, 220020, Minsk, Belarus ^b Str. P.Brovki 6, 220600, Minsk, Belarus

Such significant semiconductor properties as effective doping level and dielectric constant change in porous silicon. These properties determine electric field distribution and avalanche conditions so the key for electrophysical properties of porous silicon lays in breakdown volt-ampere curves.

Breakdown volt-ampere curves for Shotky junction aluminum - porous silicon - 0.1 Ohm*cm n-type silicon [1] are presented on fig. 1. The shape cannot be described as usual breakdown curve. Of course it is possible to explain such shape as a result of composing of a set of diodes with different breakdown voltage, connected in parallel. The distribution of the breakdown voltages is explained by different curvature radius of avalanche centers. But breakdown voltage for porous silicon is significantly higher than for monocrystal one.

When we considering porous silicon as macro semiconductor material, the space charge density is decreased proportionally to porous silicon density. It correspond to lower effective doping level, so breakdown voltage must be higher. But in porous materials the polarization ability and correspondent dielectric constant decreases too, so electric field at the same charge density is higher, and breakdown voltage must be lower.

Let relative porous silicon density is D, dielectric constant of monocrystal silicon is ε_{Si} , than effective dielectric constant ε_D is

 $\boldsymbol{\varepsilon}_{\boldsymbol{D}} = (\boldsymbol{\varepsilon}_{\boldsymbol{S}i} - 1)^* \boldsymbol{D} + 1$

If space charge density for monocrystal substrate is N, than for porous silicon we have the thickness of depletion layer at applied voltage V

 $W_D \sim \text{SQRT} (V \epsilon_D / N D)$

So it is possible to find the effective N_{eff} at which the thickness of depletion layer in monocrystal silicon at applied voltage V is the same as in porous silicon at the same voltage (the same thickness at the same voltage means that avalanche conditions is the same):

 $N_{eff} = N D \epsilon_{Si}/\epsilon_D$

For monocrystal silicon breakdown voltage is approximately proportional to $N^{2/3}$ (in voltage range 8...80 V), so it possible to calculate relative change of the breakdown voltage via **D**. The result is presented in figure 2 (normal line). It is easy to see low increasing (approximately 10% for realistic porosity) breakdown voltage, which cannot explain the experimental data. To forward the model to experimental data we suppose that significant part of silicon wires in silica skeleton are broken, so they are disconnected and can not produce spatial charge. But silicon has high dielectric constant and these wires significantly change macro electrophysical parameters. Below we consider example with total density D, but only 1/3 D is perfect wires and 2/3 D is broken wires. So, ε_D is the same, but

 $N_{eff} = 1/3 \ N \ D \ \tilde{\epsilon}_{SS}/\epsilon_D$, which corresponds to rather realistic breakdown voltage (see fig. 2 – fat line).

The distribution of the curvature radius of avalanche centers is analyzed by forward and reverse volt-amperes and volt-farads curves. The value of curvature radius is essential for electroluminescence efficiency [2].

1. Lazarouk S., Jaguiro P., Katsouba S., etc./ Applied Physics Letters. - 1996. - Vol. 68. - P.1648 2. Jaguiro P., Ferrari A., Lazarouk S. Proc. Electrochem. Soc. - 1998. - Vol. PV 97-29- P.194





Fig.2