

Integrated Multiscale Process Simulation of Damascene Structures

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Manufacturing of integrated circuits (ICs) is an inherently multi-scale, multi-step, and multi-physics process. The size scale of the units of production (wafers and dies) is centimeters to decimeters, whereas the size scale of the produced structures (devices and interconnects) is micrometer and smaller. The sequential, many-step processing brings many different materials, technology, and physics to bear. If process simulation is to act as an effective tool in addressing issues of process integration, then they must first address the multi-scale, multi-step, and multi-physics aspects of the manufacturing process.

We have integrated a pattern scale/feature scale model of electrochemical deposition (ECD) with a wafer/asperity/feature scale model of chemical mechanical polishing (CMP). The ECD model correctly predicts trends[1,2] in the effect of pattern density on the superfilling behavior of additives during ECD processing, and is based on the work of West and co-workers [3,4]. The CMP model [5] is based upon statistical descriptions of contact modeling between pad asperities and the patterned surface of the wafer. It accounts for deformation of the pad, the asperities, the wafer, and the carrier film, as well as hydrodynamic pressure in the slurry phase.

We “virtually” process clusters of trenches, filling them with ECD copper (see Figure 1), and observe the resulting bump and plateau formation above them. We then proceed to remove the blanket copper using the CMP model (Figure 2) and examine the resulting single damascene structures.

Local values of material properties—in particular, the hardness and the subsequent value of a modified Preston’s constant used in the CMP material removal rate calculation—of the as-deposited copper are taken to be functions of deposition condition, including growth rate and local additive concentration, and assumed to be grain-structural in origin. These local variations in material properties are reflected in the evolution of the superfilled structures during CMP. Further, preliminary simulations of re-crystallization and the accompanying evolution of the local values of material properties are made using a finite element based level set code.

REFERENCES

1. Y-H. Im, M.O. Bloomfield, and T. S. Cale, *Electrochem. Solid State Lett.*, accepted.
2. Y-H. Im, M.O. Bloomfield, S. Sen, and T. S. Cale, “Pattern Density Effects on Film Profile Evolution During ECD” to appear in the proceedings of the 2nd ECS Int’l. Semicon. Tech. Conf. (ISTC 2002), Electrochem. Soc, 2002.

3. A.C. West, S. Mayer, and J. Ried, *Electrochem. Solid State Lett.*, **4**, C50,(2001).
4. Y. Cao, P. Teaphaisitphongse, R. Chalupa, and A.C. West, *J. Electrochem. Soc.*, **148**, C466 (2001).
5. T.S. Cale, M.O. Bloomfield, J. Seok, C.P. Sukam, and J.A. Tichy, “Integrated Multiscale Process Simulation” in *2002 Proc. 20th Intl. VLSI Interconnection Conf. (VMIC 2002)*, T.E. Wade ed., IMIC, (2002).

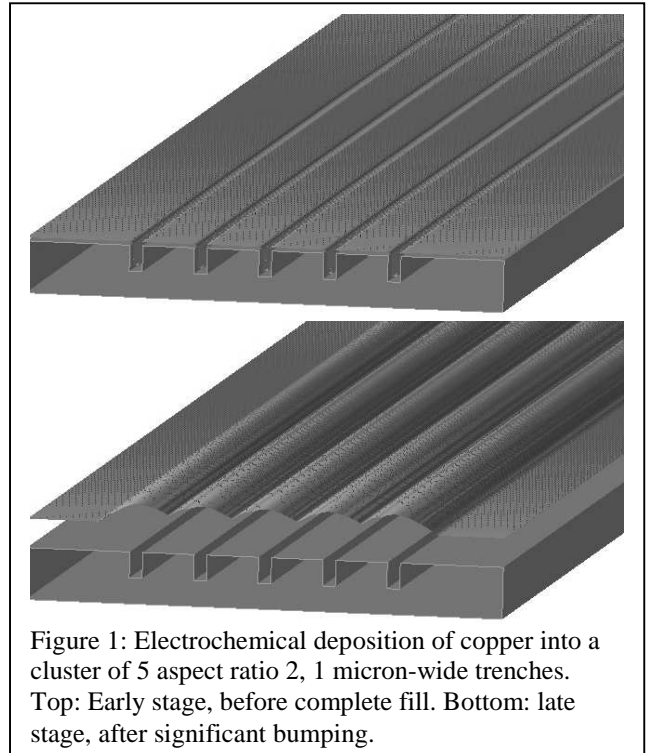


Figure 1: Electrochemical deposition of copper into a cluster of 5 aspect ratio 2, 1 micron-wide trenches. Top: Early stage, before complete fill. Bottom: late stage, after significant bumping.

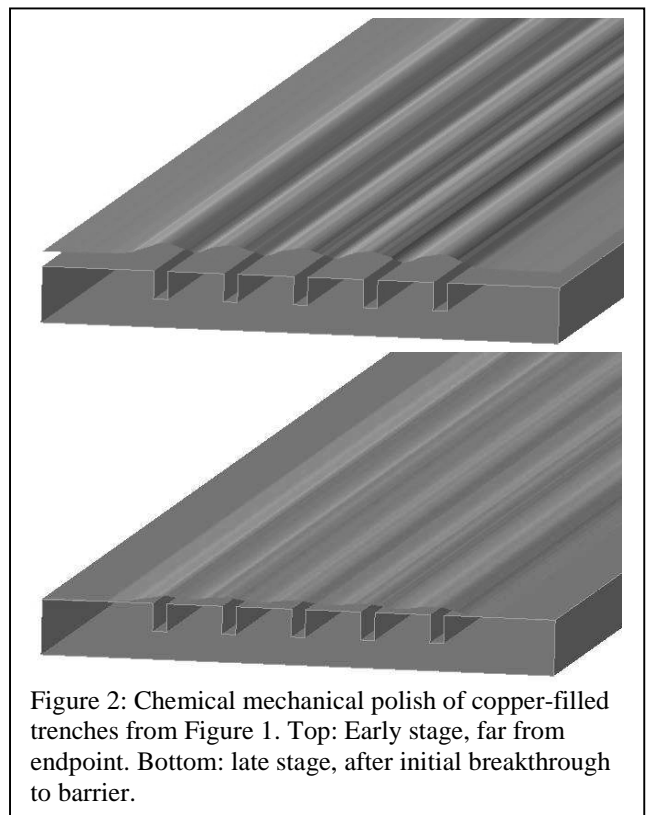


Figure 2: Chemical mechanical polish of copper-filled trenches from Figure 1. Top: Early stage, far from endpoint. Bottom: late stage, after initial breakthrough to barrier.