

Single-walled carbon nanotubes (SWNTs) – nanometer-diameter wires of pure carbon – have attracted significant interest both for the opportunity to research the basic science of one-dimensional electronic systems as well as for their possible use in nanoelectronics applications. SWNTs may be metallic or semiconducting depending sensitively on how the graphene lattice is wrapped to form the nanotube(1). Research into the electronic properties of SWNTs has focused primarily on metallic nanotubes and their excellent conduction properties as ballistic quantum wires(2). Initial studies of semiconducting SWNTs showed poorer conduction, due to local conduction barriers – likely caused by defects in the nanotubes(3).

Here we report on the growth of clean semiconducting SWNTs via chemical vapor deposition. The individual semiconducting SWNTs have extremely high mobilities, more than an order of magnitude greater than pure silicon at room temperature. In contrast to metallic SWNTs, these semiconducting nanotubes represent an electronically tunable one-dimensional system: the electronic mean-free-paths are tunable from sub-nanometers to microns by application of a gate voltage, corresponding to conductivity tunable from insulating to comparable to copper. These semiconducting SWNTs are hence an ideal laboratory for exploring the basic physics of electrons in one dimension. The high mobility of these SWNTs also lends them to applications involving charge detection, such as memories, and chemical/biochemical sensors.

Devices were prepared on degenerately-doped silicon substrates capped by 500nm of SiO₂. SWNTs were grown on the substrates using chemical vapor deposition at 900°C, with methane as the feedstock gas. Alignment marks were patterned on the substrate using electron-beam lithography, and SWNTs were located relative to the alignment marks by atomic force microscopy. A second electron-beam lithography step established Cr/Au electrical contacts to individual SWNTs.

Figure 1 shows the conductance G of a 20-micron-long SWNT as a function of voltage V_g applied to the silicon substrate (gate). The overall behavior is that of a p-channel depletion-mode transistor, as previously seen in tubeFET devices(4, 5). The mobility μ of the nanotube may be calculated from the slope of the $G(V_g)$ curve:

$$\mu = \frac{L^2}{C_g} \frac{dG}{dV_g}$$

where C_g is the gate capacitance. The gate capacitance was determined to be ~200 aF from low-temperature measurements of the Coulomb blockade. This gives a hole mobility of $\sim 2 \times 10^4$ cm²/V·s at room temperature(6), significantly higher than the hole mobility in Si (~ 450 cm²/V·s). Since the measured $G(V_g)$ may include contributions from the contacts, the measured mobility is a *lower bound* on the true nanotube mobility.

High mobility implies a large conductance change for a small change in the charge in the SWNT. This suggests SWNT transistors would be useful as sensitive charge or chemical detectors. In figure 2 we demonstrate the detection of single electronic charges being inserted and removed from traps in the SiO₂ gate dielectric(7).

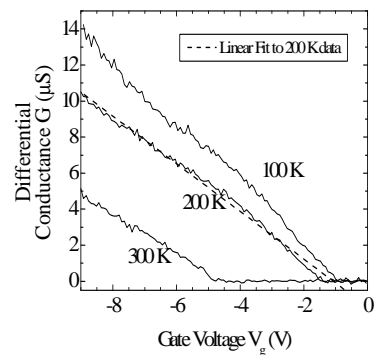


Fig. 1 Current as a function of gate voltage for a 20-micron-long semiconducting SWNT device.

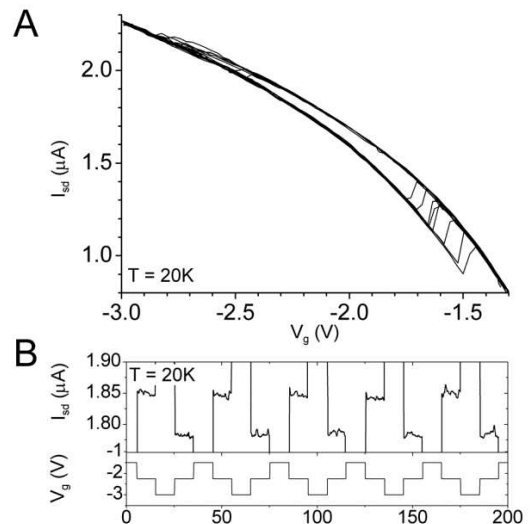


Fig. 2. SWNT single-electron memory. (A) The drain current as a function of gate voltage at a source-drain bias of 500 mV and a temperature of 20K. The gate voltage is swept between -1.3 and -3 V (towards positive eight times, and back negative seven times). Two branches of the curve are evident, corresponding to a difference of one electron in a charge trap near the nanotube. (B) Five read/write cycles of the memory at 20K. The horizontal (time) axis applies to both curves. The memory state was read at a gate voltage of -2.25 V, and written (erased) with pulses of -1.5 V (-3.0 V) to the gate.

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